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Syllabus of Master of Computer Applications (MCA), approved by MCA Coordination Committee on 7th May 2010 & Sub-Committee Academic Council held on 31 st May 2010. W.e.f. academic session 2010-11

### **GURU GOBIND SINGH**

A CPU cache is a hardware cache used by the central processing unit (CPU) of a computer to reduce the average cost (time or energy) to access data from the main memory. A cache is a smaller, faster memory, closer to a processor core, which stores copies of the data from frequently used main memory locations.

### **CPU cache - Wikipedia**

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### **Computer keyboard - Wikipedia**

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other term that includes the word information, such as information systems, information technology systems, or management information systems.

## **INDEX LESSON 1: INTRODUCTION TO DATA PROCESSING LESSON 2**

Description. Code Composer Studio, - Integrated Development Environment Code Composer Studio is an integrated development environment (IDE) that supports TI's Microcontroller and Embedded Processors portfolio.

## **Code Composer Studio (CCS) Integrated Development**

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